



Birla Institute of Technology & Science, Pilani

Prof. PB Venkataraman

Professor, Mechanical Engineering

Dean, Work Integrated Learning Programmes (WILP) Division

Ref: No.: WILPD/BCC/2-2023/51202298533

Dated: 20/Mar/2024

TO WHOMSOEVER IT MAY CONCERN

This is to certify that **Mr./Ms. VIKAS SINGH** bearing identity number **2022HT80533** is a bonafide student of the **M.Tech.(Microelectronics)** programme offered by the institute. He/She is registered in the Second Semester 2023-2024 which started on the 13th of January 2024 and it is likely to end by 26th of May 2024.

He/She is registered in the below mentioned courses in the said semester.

Course No.	Course Description	Course Unit
MELZC415	INTRODUCTION TO MEMS	4
MELZG524	REAL TIME OPERATING SYSTEMS	5
MELZG531	TESTABILITY FOR VLSI	5
MELZG613	ADV DIGITAL SIGNAL PROCESSING	5

The medium of instruction for the programme is **English**.

Sincerely,

(PB Venkataraman)



Birla Institute of Technology & Science, Pilani
Work Integrated Learning Programmes Division
Web: <https://bits-pilani-wilp.ac.in>

Tel (O): +91 1596 242210
Fax: +91 1596 244183
Email: pb.venkataraman@pilani.bits-pilani.ac.in