

OFFICE OF THE DoRD

No. DoRD/ADMIN/2021-22/033

Date: 23.03.2022

RECRUITMENT FOR JUNIOR RESEARCH FELLOW

Applications are invited for the position of JRF under the Project titled “**Architect and Design an Approximate Processor for Image Processing Applications**” in the School of Electrical Sciences, IIT Goa. The project is sponsored by Science and Engineering Research Board under the Department of Science and Technology, Government of India.

Nature of work	Design an approximate Instruction Set Architecture (ISA) for RISC processor. Implement the ISA in VHDL, and Cadence tools. Test the final approximate processor design on Image Processing applications.
No. of Position	One
Duration of appointment	Initially for a period of 1 year. Appointment may be extended for further ten months based on the performance.
Eligibility	<p>Essential qualification:</p> <ul style="list-style-type: none"> - M.E./M.Tech. in VLSI (Digital Circuits/Systems Design) with First Division. - Should have a valid GATE score. <p>Desirable:</p> <ul style="list-style-type: none"> - Exposure in VHDL, and Cadence tools.
Salary	Rs.31,000/- P.M.
DA	Not entitled to DA.
HRA	18 %
Medical Benefits	As per norms.
Leave entitlements	As per norms.
How to apply	Interested candidates can fill the below form. https://docs.google.com/forms/d/e/1FAIpQLSd648B5wzDBu52sLHAKIRNC-30pPmJT679UoP_nn1uhjoriTq/viewform?usp=sf_link
Last Date of application	06.04.2022, 5:00 PM. <i>For any update regarding the last date of submission of application candidates have to visit https://iitgoa.ac.in/project-position/</i>

Additional Instructions:

1. If the number of applications received in response to advertisement is large. The constituted selection committee may restrict the number of candidates to be called for interview to a reasonable limit of desirable qualification and /or on the record of academic performance and/or any other benchmark decided by a committee constituted to screen the applications. No communication will be entertained with candidates who are not called for Interview.
2. If the applicants have any questions, they can write to res-staff-rect@iitgoa.ac.in and in the subject they have to mention the Institute project number “2022/EMR/SD/032”.
3. Informal inquiries via email may be made with PI- Dr. Sunil Dutt, School of Electrical Sciences on sdutt@iitgoa.ac.in
4. The appointment is purely temporary and will terminate automatically without any notice or on termination of the research project.
5. The appointed person shall have no claim of appointment / absorption in funding Agency or in IIT Goa.

6. The qualification prescribed should have been obtained from recognized Universities / Institutions.
7. No TA/DA will be admissible for appearing for the interview.
8. Selected candidates will have to join in a reasonable time upon receipt of the offer.

Sd/-
Assistant Registrar (R&D)