

How to apply and other Important information

Advertisement Number: IIT/SRIC/R/EEA/2023/119, DATED JULY 26, 2023

Step-1: visit following link: <https://erp.iitkgp.ac.in/SricWeb/temporaryJobs.htm>

You will see a view as shown below (Set the views to 50 records per page- as shown in the image below)

← → ↺ 🔒 <https://erp.iitkgp.ac.in/SricWeb/temporaryJobs.htm>   Update



भारतीय प्रौद्योगिकी संस्थान खड़गपुर



INDIAN INSTITUTE OF TECHNOLOGY KHARAGPUR

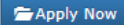
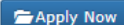
Temporary Jobs

Application fee shall be deposited in the following bank account through RTGS/NEFT/NET Banking etc. The transaction details(UTR No./Transaction ID) shall be given the application form.

- 1) Bank Name & Address : State Bank Of India(00202); R/O - Kharagpur Technology, Dist: Midnapore; West Bengal - 721302;
- 2) Name Of Bank A/C : IIT SRIC-OM Account;
- 3) Account No : 39502115210;
- 4) IFSC Code: SBIN0000202;
- 5) Swift Code : SBININBB339;

Selected candidates shall be allowed to register for MS/PhD subject to their fulfillment of the requirement for MS/PhD admission in IIT Kharagpur.

Application Status

Temporary Positions	Advertisement details	No. of Posts	Application Deadline	Apply
Position: Junior Research Fellowship - Research PI: Chetankumar Sureshbhai Mistry Department: Aerospace Engineering Ref No.: IIT/SRIC/R/TPC/2023/101 Ref Date: 11-07-2023	Project Title: Aero-Thermal Performance Assessment of Sweeping Jet Film Cooled Gas Turbine Nozzle Vane.(TPC) Sponsor: Science and Engineering Research Board (SERB)(3rd and 4th Floor, Block II, Technology Bhavan, New Mehrauli Road, New Delhi 110016) Consolidated Compensation: Upto Rs.31000(depending upon qualification & experience) Qualifications: Good knowledge of experimental techniques, such as Hot-wire Anemometry, PIV, IR camera and Pressure sensors. Experience on commissioning an experimental setup for aerodynamics and heat transfer study using wind tunnel. Knowledge on Actuators and Signal Processing. LabVIEW programming for synchronization and data acquisition, MATLAB programming for post processing. The candidate is expected to perform CFD simulations as and when required. Capable of writing the reports, conference and journal papers, etc. Experience with mentoring graduate students. Master of Technology/Engineering in Aerospace Engineering with specialization in Aerodynamics/ Propulsion or Master of Technology/Engineering in Mechanical Engineering with specialization in Fluid Sciences/Thermal science/Heat transfer or Master of Science in Aerospace, and Applied Mechanics with valid GATE/NET score or B.Tech/M.Tech/Ph.D. degree with 60% marks and 60% GATE score. Relevant Experience: Assistant - Research PI: Indrani Sen Department: Materials Science Centre Ref No.: IIT/SRIC/R/NAH/2023/101 Ref Date: 11-07-2023 Application Fees: NIL Age Limit: Upper age limit 30 years	5	01-08-2023	
Position: Junior Project Assistant - Research PI: Indrani Sen Department: Materials Science Centre Ref No.: IIT/SRIC/R/NAH/2023/101 Ref Date: 11-07-2023	Project Title: Development of High Temperature (above 350C) applications(NAH) Sponsor: Science and Engineering Research Board (SERB)(3rd and 4th Floor, Block II, Technology Bhavan, New Mehrauli Road, New Delhi 110016) Consolidated Compensation: Upto Rs.31000(depending upon qualification & experience) Qualifications: B.Tech/M.Tech/Ph.D. degree with 60% marks and 60% GATE score. Experience in High Temperature (above 350C) applications(NAH) Relevant Experience: Assistant - Research PI: Indrani Sen Department: Materials Science Centre Ref No.: IIT/SRIC/R/NAH/2023/101 Ref Date: 11-07-2023 Application Fees: Rs.100/=(Not for female candidates)	5	07-08-2023	

Showing 1 to 5 of 43 rows 5 records per page

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Step-2: Scroll down (or search for, CTRL+F) to find the SRF position having ‘Siddhartha Mukhopadhyay’ as PI (principle Investigator) and deadline as 16-08-2023

Position: Senior Research Fellowship - Research PI: Siddhartha Mukhopadhyay Department: Electrical Engineering Ref No.: IIT/SRIC/R/EEA/2023/119 Ref Date: 26-07-2023	Project Title: Centre of Excellence on Energy Aware Urban Infrastructure(EEA) Sponsor: Science and Engineering Research Board (SERB)(Department of Science and Technology, 5 and 5A, Lower Ground Floor,Vasant Square Mall, Plot No. A,Community Centre, Sector - B, Pocket-5Vasant Kunj, New Delhi - 110 070) Consolidated Compensation: Upto Rs.35000(depending upon qualification & experience) Qualifications: 1. BE/BTech in Electrical Engineering or Electrical and Electronics Engineering or Electrical and Instrumentation Engineering 2. GATE qualification in any of the following disciplines: EE, EC, IN Or ME/MTech in Electrical Engineering (or equivalent) with specialization relevant to any or combination of the following: Power Electronics, Machine Drives, Power and Energy, Control system 3. Minimum two years of research experience in the field of Electrical Engineering preferably in the area of Power Electronic converter development and testing. Relevant Experience: A. ME/MTech with specialization, 'Power Electronics and/or Machine drives' or equivalent B. Power Electronic converter hardware development and testing for solar photovoltaic system C. Embedded coding preferably for Power Electronic converter systems D. PCB design, fabrication, and testing E. Expertise in solar photovoltaic systems Application Fees: NIL Age Limit: Upper age limit is 32 year. Relaxable upto 5 years in the case of SC/ST/OBC/Physically Handicapped/ Visually Handicapped and female applicant	1	16-08-2023	Apply Now
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Step-3: Go through the ‘Qualification’ requirement which is mandatory (no relaxation possible). Proceed to the next step only when you meet the requirement mentioned in the ‘Qualification’ section.

Note: The four points mentioned in The ‘Relevant Experience’ section are preferable but not mandatory. If you meet any one of them,your candidature will be given due preference.

Step-4: Click on ‘Apply Now’ (make sure you have clicked on the correct post i.e. the Senior Research Fellowship post having ‘Siddhartha Mukhopadhyay’ as PI), and **Advertisement Number: IIT/SRIC/R/EEA/2023/119, DATED JULY 26, 2023** . Complete the application process.

Step-5 (optional): You may send an email to Dr. Dipankar Debnath (ddeb Nath@ee.iitkgp.ac.in) to get an acknowledgement of your application (i.e. confirmation of receipt of your application).

Important Information:

Department/School/Centre	Electrical Engineering (IIT Kharagpur)
Essential Qualifications	1. BE/BTech in Electrical Engineering or Electrical and Electronics Engineering or Electrical and Instrumentation Engineering 2. GATE qualification in any of the following disciplines: EE, EC, IN Or ME/MTech in Electrical Engineering (or equivalent) with specialization relevant to any or combination of the following: Power Electronics, Machine Drives, Power and Energy, Control system 3. Minimum two years of research experience in the field of Electrical Engineering preferably in the area of Power Electronic converter development and testing.
Relevant Experience	A. ME/MTech with specialization, 'Power Electronics and/or Machine drives' or equivalent B. Power Electronic converter hardware development and testing for solar photovoltaic system C. Embedded coding preferably for Power Electronic converter systems

D. PCB design, fabrication, and testing
E. Expertise in solar photovoltaic systems

Sponsor	Science and Engineering Research Board (SERB), Govt. of India	
Application Deadline	16-Aug-2023	<i>Duration of Fellowship:</i> Approx. 42 months. (Very suitable for PhD / MS aspirants)
Application Fees	NIL	Reference No. <u>IIT/SRIC/R/EEA/2023/119, DATED JULY 26, 2023</u>
Age Limit (Year)	Upper age limit is 32 years. Relaxable upto 5 years in the case of SC/ST/OBC/Physically Handicapped / Visually Handicapped and female applicants.	
Consolidated Compensation	Rs.35,000 (at present). Hopefully it will increase to 42,000/- soon.	
Other details	The selected candidate needs to contribute towards building Power Electronic Interface for solar PV systems. The exact research/problem statement will be shared only with the selected candidate. The candidate will work under the supervision of Dr. Dipankar Debnath, Assistant Professor, Electrical Engineering Department, IIT Kharagpur. This project also has other faculties from Electrical and also from other departments as the overall project is interdisciplinary. Thus the selected candidate will learn several interdisciplinary aspects as well.	

Selection steps and important dates:

1. Shortlisting of candidates (around 16-17 August 2023). Shortlisted candidates will be sent call letter for interview (around 23rd August 2023)
2. Tentative interview date is 6th September 2023 (most likely in ONLINE mode)
3. Offer letter will be sent to the selected candidate within 15th September 2023 and the candidate is expected to join as soon as possible (no later than 21 days) after receipt of offer letter. A candidate may request for extension if required.