

AKSHITA

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CAREER OBJECTIVE

To obtain a creative and challenging position in an organization that gives me an opportunity for self improvement and leadership, while contributing to the symbolic growth of the organization with my technical, innovative and logical skills.

ACADEMIC QUALIFICATION

COURSE/EXAMINATION	GRADUATION YEAR	INSTITUTION	PERFORMANCE
Bachelor of Engineering (B.E-Electronics and Communication)	2024	Thapar Institute of Engineering and Technology, Patiala	7.68 CGPA (as of 6 th semester)
Class XII (CBSE)	2020	DAV Public School, Mansa	76.2%
Class X (CBSE)	2018	DAV Public School, Mansa	79.8%

SUBJECTS/ELECTIVES

Computer Programming, Object Oriented Programming, Data Structures and Algorithms, Circuit Analysis and Synthesis, Digital System Design, Numerical Analysis, Optimization Techniques, Analog Communication System, MOS Circuit Design, Design using Verilog, Verilog.

SKILLS/ INTERESTS

C++, SQL(Structured Query Language), DSA (Data Structures and Algorithms), AutoCAD, Tinkercad, Digital Electronics, Presentation skills, Teamwork, Leadership, Time management.

PROJECTS

Sales Inventory Management System

Jun 2023-Jul 2023

- Engineered a sales management system leveraging my C++ skills, designed to seamlessly elevate sales operations, reinforce precision in inventory tracking, and enable the generation of comprehensive sales reports.
- Implemented features to effectively store, monitor, and trace vital product names, prices, quantities, and sales data. This system offers real-time updates, ensuring accurate and up-to-the-minute insights into stock availability.
- This project effectively demonstrated my proficiency in C++ programming while effectively addressing real-world inventory management challenges.

Sudoku Solver

Jun 2023-Jul 2023

- Successfully created a functional Sudoku solver capable of solving a wide range of Sudoku puzzles.
- Implemented using C++ that employs recursion and backtracking algorithms.
- Demonstrated problem-solving skills by tackling complex logical challenges in the context of the project.

64-Bit Multiplier using Vedic Algorithm

Mar 2023-Apr 2023

- Executed the design of the multiplier using Verilog HDL on the platforms, Xilinx Vivado and EdaPlayground.
- The highly efficient 64-bit multiplier for the mantissa calculation using a rule or sutra of Vedic mathematics called the Urdhva (vertical) Tiryakbhyam (crosswise) Sutra which deals with vertical and crosswise multiplication.
- Using this sutra in the computation algorithm of DSP (Digital Signal Processing) processors, can enhance the efficiency and at the same time can reduce the complexity, area, power consumption, and delay.

LED Tester using Operational Amplifier

Sep 2022-Oct 2022

- Exclusively focused on precise hardware implementation, skillfully employed pivotal components such as breadboard, LEDs, resistors, capacitors, and the IC741- Operational Amplifier to ensure robust functionality.
- Designed a circuit that effectively helped to determine whether the op-amp is in good condition or it is faulty.

EXTRA-CURRICULAR ACTIVITIES AND ACHIEVEMENTS

- Cleared JEE Mains (2020) and achieved a percentile of 92.0598532 among 10.23 lakh students who appeared.
- With great enthusiasm, actively participated in a captivating off-campus online case study competition, gaining valuable insights into the art of effective decision-making and proficient problem-solving techniques
- Efficiently executed the Punjabi Queens event as floor in-charge at T.I.E.T. hostel. Recognizing my efforts, the hostel management entrusted me with the roles of both Mess Secretary and Discipline In-charge for the current semester.
- As TIET's hostel Discipline in-charge, resolved numerous student's concerns related to hostel and upheld discipline.
- As the Group Representative of our T.I.E.T. subgroup, I effectively demonstrated robust leadership skills.
- Achieved 4 stars on HackerRank, showcasing advanced coding proficiency and adeptness in problem-solving.

HOBBIES

Singing, Listening music, Dancing, Gardening, Playing Badminton, Cooking